Amendments to the Claims:

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

1. (previously presented) A circuit arrangement for obtaining an output signal from a signal containing at least one alternating component, said circuit arrangement comprising

   a signal source that supplies this signal,

   a first peak value detection device for determining a maximum value (Vmax) of the signal,

   a second peak value detection device for determining a minimum value of the signal,

   a first signal linking device for obtaining a first resulting signal by additive linking of the signal, the maximum value and the minimum value in accordance with the rule

   $$V1=K1*\{Vs-(Vmax+Vmin)/2\},$$

   in which K1 is a freely selectable first constant factor,

   a second signal linking device for obtaining a second resulting signal by additive linking of the maximum value and minimum value (Vmin) in accordance with the rule

   $$V2=(Vmax-Vmin)*K2,$$

   in which K2 is a freely selectable second constant factor,

   a first squaring device for squaring the first resulting signal, a second squaring device for squaring the second resulting signal and a third signal linking device for

obtaining the output signal by additive linking of the squared first resulting signal and the squared second resulting signal in accordance with the rule

$$Va=K3*\{(1/8)*(K1/K2)^2*(V2)^2-(V1)^2\},$$

in which K3 is a freely selectable third constant factor.

2. (previously presented) A circuit arrangement as claimed in claim 1, characterized in that the signal source is formed by a sensor device.

3. (previously presented) A circuit arrangement as claimed in claim 2, characterized in that the sensor device is designed as a magnetoresistive sensor device.

4. (previously presented) A rotational speed measurement device, characterized by a circuit arrangement as claimed in claim 1.

5. (currently amended) A method of <u>measuring rotational speed of an object, the method comprising:</u>

    <u>obtaining a signal containing at least one alternating component;</u>

    ~~obtaining an output signal from a signal containing at least one alternating component, said method comprising the following method steps:~~

    determining a maximum value of the signal,

    determining a minimum value of the signal,

    obtaining a first resulting signal by additive linking of the signal, the maximum value and the minimum value in accordance with the rule

$$V1=K1*\{Vs-(Vmax+Vmin)/2\},$$

in which K1 is a freely selectable first constant factor,

    obtaining a second resulting signal by additive linking of the maximum value and minimum value in accordance with the rule

V2=(Vmax-Vmin)*K2,

in which K2 is a freely selectable second constant factor,

squaring the first resulting signal,

squaring the second resulting signal, ~~and~~

obtaining the output signal by additive linking of the squared first resulting signal and the squared second resulting signal in accordance with the rule

$$Va=K3*\{(1/8)*(K1/K2)^2*(V2)^2-(V1)^2\},$$

in which K3 is a freely selectable third constant factor; and

generating a rotational speed measurement as a function of the output signal.